

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/743,113	JUNG, KYUNG YUN	
Examiner		Art Unit		Page 1 of 1
Johannes P. Mondt		3663		

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolf, "Silicon Processing for the VLSI Era", Volume 3, Chapter 3, pages 83-89; Lattice Press, Sunset Beach, CA (1995).
	V	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

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